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JAN 1 0 2005

Paula Faulk Hurley

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Marcos Karnezos

Application No.: 10/632,553

Filed: 02 August 2003

Title: Semiconductor Multi-Package Module Having Package Stacked Over Die-Down Flip Chip Ball Grid Array Package And Having Wire Bond Interconnect Between Stacked Packages Attorney Docket No.: CPAC 1017-6 D4

Examiner: Unassigned

Group: 2812

Confirmation No.: 2573

Customer No. 22470

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

It is requested that the information identified in this statement be considered by the Examiner and made of record in the above-identified application. This statement is not intended to represent that a search has been made or that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56.

Enclosed with this statement is a Form PTO/SB/08A. The Examiner is requested to initial the form and return it to the undersigned in accordance with M.P.E.P. 609.

Also enclosed with this statement is a copy of each cited document as required by 37 C.F.R. 1.98. However, since this application was filed after June 30, 2003, copies of U. S. Patents and U. S. Patent Application Publications are not enclosed. (See O. G. Notice of August 5, 2003.)

This statement should be considered under 37 C.F.R. 1.97(b) because it is being filed

before the mailing date of the first Office Action on the merits.

Fee Authorization. The Commissioner is hereby authorized to charge any additional fees or credit any overpayment associated with this communication to Deposit Account No. 50-0869 (CPAC 1017-6 D4). A duplicate copy of this authorization is enclosed.

Respectfully submitted,

Haynes Beeffel & Wolfeld LLP

Bill Kennedy, Reg. No. 33,407

Date: 10 January 2005

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Date: 10 Janaury 2005

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